Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.013”**

**ANODE**

**.009 x .009”**

**.013”**

**Top Material: Al**

**Backside Material: AuAs**

**Bond Pad Size: .009 X .009”**

**Backside Potential: CATHODE**

**APPROVED BY: DK DIE SIZE .013” X .013” DATE: 1/26/22**

**MFG: SILICON SUPPLIES THICKNESS .008” P/N: 1N52\*\*B**

**DG 10.1.2**

#### Rev B, 7/1